



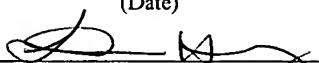
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PATENT
Docket No. JCLA11671
page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : DAVID SHIUNG et al.
Application No. : 10/655,898
Filed : September 04, 2003
For : AN DIGITAL FREQUENCY SYNTHESIZING
CIRCUIT AND SYSTEM THEREOF USING
INTERPOLATION AND LINEAR FEEDBACK
SHIFT REGISTER (LFSR)
Examiner : COX, CASSANDRA F.
Art Unit : 2816

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June 8, 2005
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Jiawei Huang, Reg. No. 43,330

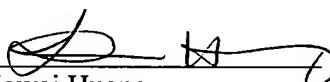
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Sir:

In response to the Notice of Allowability mailed on March 11, 2005, transmitted herewith
are:

(X) TWO (2) sheet(s) of formal drawing for Figs. 3-6B for the above identified application.
(X) Return prepaid postcard.

Date: 6/8/2005

By: 
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Registration No. 43,330

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